

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("6424649").PN.	US-PGPUB; USPAT; EPO	OR	OFF	2005/06/10 10:01
L2	1	1 and (chip circuit asic ic)	US-PGPUB; USPAT; EPO	OR	ON	2005/06/10 10:03
L3	12066	(cross\$4 near3 connect\$4) same (chip circuit asic ic)	US-PGPUB; USPAT; EPO	OR	ON	2005/06/10 10:04
L4	52	((cross\$4 near3 connect\$4) near10 (chip circuit asic ic)) and (serial\$1 near3 parallel\$4) and (sonet sdh)	US-PGPUB; USPAT; EPO	OR	ON	2005/06/10 10:41
L5	21	((cross\$4 near3 connect\$4) near10 (chip (integrat\$4 adj1 circuit\$1) asic ic)) and (serial\$1 near3 parallel\$4) and (sonet sdh)	US-PGPUB; USPAT; EPO	OR	ON	2005/06/10 10:40
L6	81	((cross\$4 near3 connect\$4) near10 (chip (integrat\$4 adj1 circuit\$1) asic ic)) and (serial\$1 near3 parallel\$4)	US-PGPUB; USPAT; EPO	OR	ON	2005/06/10 10:40
L7	167	((cross\$4 near3 connect\$4) same (chip (integrat\$4 adj1 circuit\$1) asic ic)) and (serial\$1 near3 parallel\$4)	US-PGPUB; USPAT; EPO	OR	ON	2005/06/10 10:41
L8	2358	((cross\$4 near3 connect\$4) same (chip (integrat\$4 adj1 circuit\$1) asic ic))	US-PGPUB; USPAT; EPO	OR	ON	2005/06/10 10:41
L9	843	((cross\$4 near3 connect\$4) near10 (chip (integrat\$4 adj1 circuit\$1) asic ic))	US-PGPUB; USPAT; EPO	OR	ON	2005/06/10 10:41
L10	328	((cross\$4 near3 connect\$4) near10 (chip circuit asic ic)) and (serial\$1 near3 parallel\$4)	US-PGPUB; USPAT; EPO	OR	ON	2005/06/10 10:41